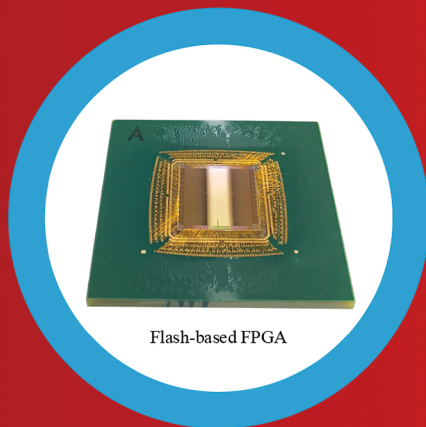
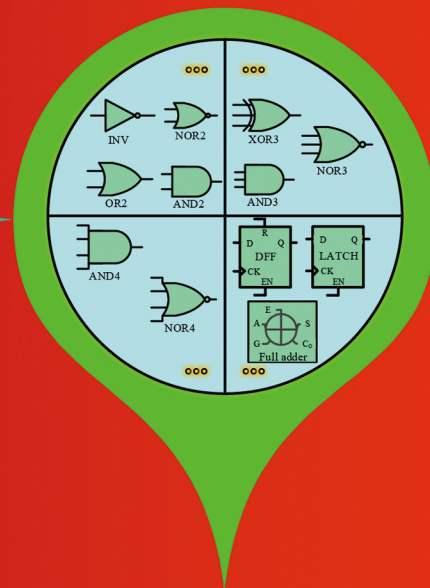
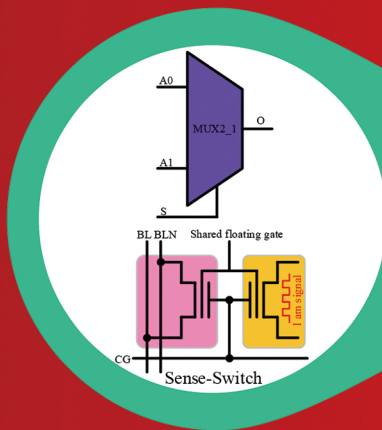




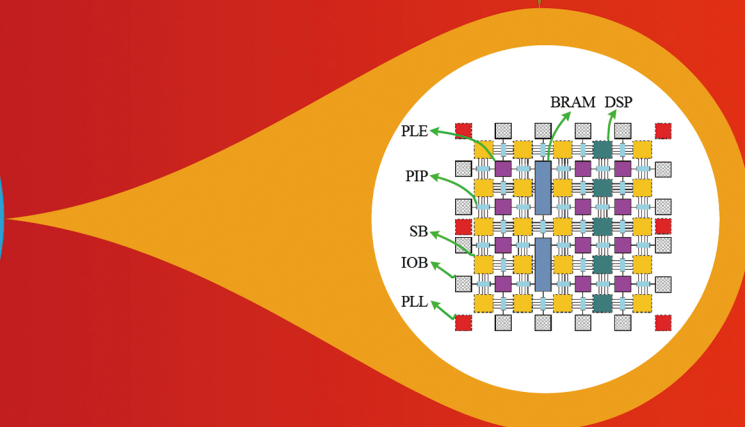
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Flash-based FPGA



Design and verification of an FPGA programmable logic element based on Sense-Switch pFLASH

Zhengzhou CAO, Guozhu LIU, Yanfei ZHANG, et al.

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