Optimization of block-floating-point realizations for digital controllers with finite-word-length considerations*

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Abstract: The closed-loop stability issue of finite-precision realizations was investigated for digital controllers implemented in block-floating-point format. The controller coefficient perturbation was analyzed resulting from using finite word length (FWL) block-floating-point representation scheme. A block-floating-point FWL closed-loop stability measure was derived which considers both the dynamic range and precision. To facilitate the design of optimal finite-precision controller realizations, a computationally tractable block-floating-point FWL closed-loop stability measure was then introduced and the method of computing the value of this measure for a given controller realization was developed. The optimal controller realization is defined as the solution that maximizes the corresponding measure, and a numerical optimization approach was adopted to solve the resulting optimal realization problem. A numerical example was used to illustrate the design procedure and to compare the optimal controller realization with the initial realization.

Key words: Digital controller, Finite word length, Block-floating-point, Closed-loop stability, Optimization

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INTRODUCTION

Due to the finite word length (FWL) effect, a casual controller implementation may degrade the designed closed-loop performance or even destabilize the designed stable closed-loop system, if the controller implementation structure is not carefully chosen. The effects of finite-precision computation have become more critical with the growing popularity of robust controller design methods which focus only on dealing with large plant uncertainty (Keel and Bhattacharryya, 1997).

A control law can be accomplished with different realizations and the parameters of a controller realization are represented by a digital processor of finite bit length in a particular number representation format, such as fixed-point, floating-point or block-floating-point format. In a given representation format, different controller realizations have different degrees of "robustness" against FWL errors. This property can be utilized to select "optimal" realizations in the given format. In fixed-point format or floatingpoint format, the optimal controller realization problems were studied in Gevers and Li (1993), Istepanian and Whidborne (2001), Fialho and Georgiou (1994; 1999), Li (1998), Whidborne et al. (2000; 2001), Wu et al. (2001) and Whidborne and Gu (2001). A comparative study was presented by Istepanian et al. (2000) on the stability and performance using blockfloating-point and fixed-point implementations for various realizations for a PID digital controller of a steel rolling mill benchmark system. However the optimal controller realization problem in block-floating-point format was not discussed there. To date the true block-floating-point FWL

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closed-loop stability measure has not been seen which can be optimized to obtain the optimal block-floating-point realization. This work is aimed to study the optimal controller realization problem in block-floating-point format.

BLOCK-FLOATING-POINT

The fixed-point and floating-point formats are the two basic representation schemes for real numbers stored in digital memory and in digital registers. For a group of real numbers stored simultaneously in a digital processor, the so-called block-floating-point format is also available. Suppose that the group of real numbers form a set S. In the block-floating-point format, S is divided into some blocks. The block-floating-point scheme may be viewed as aiming to achieve a trade-off between the simplicity of the fixed-point scheme and the accuracy of the floating-point scheme

For illustrative purpose and without loss of generality, consider the case of dividing S into two non-empty subsets S_1 and S_2 , which satisfy $S_1 \cup S_2 = S$ and $S_1 \cap S_2$ is the empty set. Let η_1 be the element in S_1 that has the largest absolute value, and η_2 be the element in S_2 that has the largest absolute value. Then, any $x \in S$ can be expressed uniquely as

$$x = (-1)^s \times u \times 2^h \tag{1}$$

where $s \in \{0, 1\}$ is the sign of x, $u \in [0, 1)$ is the block mantissa of x, and the block exponent of x is

$$h \triangleq \begin{cases} \log_2 |\eta_1| \downarrow + 1, \text{ for } x \in S_1, \\ \log_2 |\eta_2| \downarrow + 1, \text{ for } x \in S_2, \end{cases} (2)$$

• \rfloor denotes the floor function, i.e., $x \rfloor$ is the closest integer less than or equal to x. Obviously, all the elements in the same block have the same exponent value of h. When all the elements in S are stored in a digital processor of the bit length

$$\beta = 1 + \beta_u + \beta_h \tag{3}$$

in a block-floating-point scheme, the bits are assigned as follows: 1 bit for the sign, β_u bits for u which is represented in fixed-point with the two's complement system, and β_h bits for h. Thus the set of all the block-floating-point num-

bers that can be represented by the bit length β is given by

$$\mathscr{F} \stackrel{\triangle}{=} \left\{ \left(\sum_{j=1}^{\beta_n} b_j 2^{-j} - s \right) \times 2^h : s \in \{0, 1\}, \\ b_j \in \{0, 1\}, \ h \in \mathbb{Z}, \underline{h} \leqslant h \leqslant \overline{h} \right\}$$
(4)

where Z denotes the set of integers, \underline{h} and h represent the lower and upper limits of the block exponent, respectively, and $\overline{h} - \underline{h} = 2^{\beta_h} - 1$. Obviously, when $h > \overline{h}$ or $h < \underline{h}$, overflow or underflow will occur in the block-floating-point representation.

When no underflow or overflow occurs, that is, h is within $Z_{\lceil \underline{h}, \overline{h} \rceil}$, the block-floating-point quantization operator $\mathcal{Q}: S \longrightarrow \mathcal{F}$ is defined as

$$\mathcal{Q}(x) \stackrel{\triangle}{=} (-1)^s 2^{(h-\beta_u)} 2^{(\beta_u-h)} | x | + 0.5 \rfloor.$$
 (5

The quantization error of the block-floating-point representation is defined as

$$\varepsilon \stackrel{\triangle}{=} | x - \mathcal{Q}(x) |. \tag{6}$$

Denote

$$r(x) \triangleq \begin{cases} 2^{\log_2 |\eta_1| \perp + 1}, & \text{for } x \in S_1, \\ 2^{\log_2 |\eta_2| \perp + 1}, & \text{for } x \in S_2. \end{cases}$$
 (7)

It can be shown easily that the quantization error is bounded by

$$\varepsilon < r(x)2^{-(\beta_u+1)} \tag{8}$$

Thus, when $x \in S$ is implemented in the block-floating-point format of β_u block mantissa bits, assuming no underflow or overflow, it is perturbed to

$$Q(x) = x + r(x)\delta, |\delta| < 2^{-(\beta_u + 1)}.$$
 (9)

Hence the perturbation resulting from FWL block-floating-point representation is neither multiplicative nor additive. The perturbation depends on the set S and how S is divided into blocks. It can also be seen that the dynamic range of block-floating-point representation is determined by β_h bits, and the precision of block-floating-point representation is determined by β_u bits.

PROBLEM STATEMENT

Consider the discrete-time closed-loop con-

trol system consisting of a linear time-invariant plant P and a digital controller C. The plant model P is assumed to be strictly proper with a state-space description

$$\begin{cases} x(k+1) = Ax(k) + Be(k) \\ y(k) = Cx(k) \end{cases}$$
 (10)

which is completely state controllable and observable with $A \in R^{n \times n}$, $B \in R^{n \times p}$ and $C \in R^{q \times n}$. The digital controller C is described by

$$\begin{cases}
\mathbf{v}(k+1) = \mathbf{F}\mathbf{v}(k) + \mathbf{G}\mathbf{y}(k) \\
\mathbf{e}(k) = \mathbf{J}\mathbf{v}(k) + \mathbf{M}\mathbf{y}(k)
\end{cases} (11)$$

with $F \in R^{m \times m}$, $G \in R^{m \times q}$, $J \in R^{p \times m}$ and $M \in R^{p \times q}$.

Assume that a realization (F_0 , G_0 , J_0 , M_0) of C has been designed. It is well-known that the realizations of C are not unique. All the realizations of C form the realization set

$$S_C \triangleq \{ (F, G, J, M) : F = T^{-1}F_0T,$$

 $G = T^{-1}G_0, J = J_0T, M = M_0 \}$ (12)

where $T \in \mathbb{R}^{m \times m}$ is any real-valued nonsingular matrix, called a similarity transformation. Denote

$$X = \begin{bmatrix} x_{j,k} \end{bmatrix} \stackrel{\triangle}{=} \begin{bmatrix} M & J \\ G & F \end{bmatrix}. \tag{13}$$

We also refer to X as a realization of C. The stability of the closed-loop system depends on the eigenvalues of the matrix

$$\overline{A}(X) = \begin{bmatrix} A + BMC & BJ \\ GC & F \end{bmatrix} \\
= \begin{bmatrix} A & 0 \\ 0 & 0 \end{bmatrix} + \begin{bmatrix} B & 0 \\ 0 & I \end{bmatrix} X \begin{bmatrix} C & 0 \\ 0 & I \end{bmatrix} \\
\stackrel{\triangle}{=} M_0 + M_1 X M_2 \tag{14}$$

where $\mathbf{0}$ and \mathbf{I} denote the zero and identity matrices of appropriate dimensions, respectively. All the different realizations \mathbf{X} have the same set of closed-loop poles if they are implemented with infinite precision. Since the closed-loop system is designed to be stable, the eigenvalues

$$|\lambda_i(\overline{A}(X))| < 1, \ \forall \ i \in \{1, \cdots, m+n\}.$$

$$(15)$$

For a matrix $\mathbf{W} = [w_{j,k}]$, let $\mathbf{U}(\mathbf{W})$ be the matrix of the same dimension whose elements are all 1s,

$$\parallel \mathbf{W} \parallel_{\max} \triangleq \max_{j,k} |w_{j,k}|, \qquad (16)$$

$$\pi(\boldsymbol{W}) \triangleq \min_{i,k} \{ ||w_{j,k}|| : w_{j,k} \neq 0 \}, \quad (17)$$

For two matrice $W = [w_{j,k}]$ and $Z = [z_{j,k}]$ of the same dimension, define the Hadamard product of W and Z

$$\boldsymbol{W} \circ \boldsymbol{Z} \triangleq \left[w_{i,k} \, z_{i,k} \right] . \tag{18}$$

We have known that the controller realization X is implemented in block-floating-point format of β_h block exponent bits, β_u block mantissa bits and one sign bit. In the remainder of this paper, it is assumed that X stored in the block-floating-point format is divided into "natural" blocks of F, G, J and M. Let ξ_1 be the element in F which has the largest absolute value. Similarly ξ_2 , ξ_3 and ξ_4 is defined in G, J and M respectively. Denote

$$q(X) \triangleq [\xi_1 \quad \xi_2 \quad \xi_3 \quad \xi_4]^T \quad (19)$$

with T being the transpose operator.

Firstly, the dynamic range of β_h bits must be large enough for X. We define a dynamic range measure for realization X in block-floating-point format as

$$\gamma(X) \triangleq \log_2 \frac{4 \parallel \boldsymbol{q}(X) \parallel_{\text{max}}}{\pi(\boldsymbol{q}(X))}.$$
(20)

The rationale of this dynamic range measure becomes clear in the following (obvious) proposition.

Proposition 1 The realization X can be represented in the block-floating-point format of β_h block exponent bits without underflow or overflow, if $2^{\beta_h} \geqslant \log_2\left(\frac{\parallel \boldsymbol{q}(X) \parallel_{\max}}{\pi(\boldsymbol{q}(X))}\right) + 2$.

Let β_h^{\min} be the smallest block exponent bit length that, when used to implement X, does not cause overflow or underflow. The minimum required block exponent bit length can easily be computed by

$$\beta_{h}^{\min}(X) = \lceil \log_{2}(\lfloor \log_{2} \parallel \boldsymbol{q}(X) \parallel_{\max} \rfloor - \lfloor \log_{2}\pi(\boldsymbol{q}(X)) \rfloor + 1) \rceil, \tag{21}$$

where $\lceil \cdot \rceil$ denotes the ceil function, i.e., $\lceil x \rceil$ is the closest integer greater than or equal to x. Note that the measure $\gamma(X)$ defined in Eq. (20) provides an estimate of β_k^{\min} as

$$\hat{\beta}_h^{\min}(\mathbf{X}) \triangleq \lceil \log_2 \gamma (\mathbf{X}) \rceil . \tag{22}$$

It can easily be seen that $\hat{\boldsymbol{\beta}}_{\,h}^{\,\mathrm{min}} \geqslant \boldsymbol{\beta}_{\,h}^{\,\mathrm{min}}$.

When the dynamic range is sufficient, according to Eq. (9), X is perturbed to X + E (X) $\circ \Lambda$ due to the effect of finite β_u where

$$E(X) \stackrel{\triangle}{=} \begin{bmatrix} 2^{\lfloor \log_2 + \xi_4 + \rfloor + 1} U(M) & 2^{\lfloor \log_2 + \xi_3 + \rfloor + 1} U(J) \\ 2^{\lfloor \log_2 + \xi_2 + \rfloor + 1} U(G) & 2^{\lfloor \log_2 + \xi_1 + \rfloor + 1} U(F) \end{bmatrix}$$
(23)

Each element $\delta_{j,k}$ of Δ is bounded by $\pm 2^{-(\beta_u+1)}$, that is,

$$\parallel \mathbf{\Lambda} \parallel_{\max} < 2^{-(\beta_u + 1)}. \tag{24}$$

With the perturbation Δ , $\lambda_i(\overline{A}(X))$ is moved to $\lambda_i(A(X+E(X)\circ\Delta))$. If an eigenvalue of $A(X+E(X)\circ\Delta)$ is outside the open unit disk, the closed-loop system, designed to be stable, becomes unstable with the finite-precision implemented X.

It is therefore critical to know when the FWL error will cause closed-loop instability. This means that we would like to know the largest open "cube" in the perturbation space within which the closed-loop system remains stable. Based on this consideration, a precision measure for realization \boldsymbol{X} in block-floating-point format can be defined as

$$\mu_0(X) \triangleq \inf\{ \| \Delta \|_{\max} : \overline{A}(X + E(X) \circ \Delta) \text{ is }$$
unstable \(\). (25)

From the above definition, the following proposition is obvious.

Proposition 2 $\overline{A}(X + E(X) \circ \Delta)$ is stable if $\|\Delta\|_{\max} < \mu_0(X)$.

Thus under the condition that the dynamic range is sufficient, that is, $\beta_h \geqslant \beta_h^{\min}$, the perturbation $\parallel \pmb{\Delta} \parallel_{\max}$ and therefore the block mantissa bit length β_u determines whether the closed-loop remains stable. Let β_u^{\min} be the block mantissa bit length such that $\forall \beta_u \geqslant \beta_u^{\min}$, the closed-loop system is stable with \pmb{X} implemented by β_u block mantissa bits and the closed-loop system is unstable with \pmb{X} implemented by β_u^{\min} and β_u^{\min} block mantissa bits. The precision measure β_u^{\min} as

$$\hat{\beta}_{u0}^{\min}(X) \triangleq -\lfloor \log_2 \mu_0(X) \rfloor - 1 . \quad (26)$$

It can be seen that $\hat{\beta}_{u0}^{min} \geqslant \beta_{u}^{min}$.

Define the minimum total bit length required in the implementation of X as

$$\beta^{\min} \triangleq \beta_h^{\min} + \beta_u^{\min} + 1. \tag{27}$$

Clearly, X implemented with a bit length $\beta \geqslant \beta^{\min}$ can guarantee a sufficient dynamic range and closed-loop stability. Combining the measures $\gamma(X)$ and $\mu_0(X)$ results in the following true FWL closed-loop stability measure for the given realization X inblock-floating-point format

$$\rho_0(\mathbf{X}) \triangleq \mu_0(\mathbf{X})/\gamma(\mathbf{X}). \tag{28}$$

An estimate of eta^{\min} is given by $ho_0(X)$ as

$$\hat{\beta}_0^{\min}(X) \triangleq -\lfloor \log_2 \rho_0(X) \rfloor + 1. \quad (29)$$

It is clear that $\hat{\beta}_0^{\min} \geqslant \beta^{\min}$. The following proposition summarizes the usefulness of $\rho_0(X)$ as a measure for the FWL characteristics of X in block-floating-point format.

Proposition 3 The controller realization X implemented in block-floating-point format with a bit length β can guarantee a sufficient dynamic range and closed-loop stability, if

$$2^{\beta-1} \geqslant \frac{1}{\rho_0(X)}.\tag{30}$$

The closed-loop stability measure $\rho_0(X)$ depends on the controller realization X only. Consequently, an optimal realization can in theory be found by maximizing $\rho_0(X)$ over S_{ℓ} , leading to the following optimal controller realization problem

$$v_{\text{true}} \stackrel{\triangle}{=} \max_{\mathbf{x} \in S_c} \rho_0(\mathbf{X}). \tag{31}$$

However, the difficulty with this approach is that computing the value of $\mu_0(X)$ is an unsolved open problem. Thus, the true FWL closed-loop stability measure $\rho_0(X)$ and the optimal realization problem (31) have limited practical significance. In the next section, an alternative measure is developed which not only can quantify the FWL characteristics of X in block-floating-point format but also is computationally tractable.

A TRACTABLE FWL CLOSED-LOOP STABILITY MEASURE

When the FWL error Δ is small, from a

first-order approximation, $\forall i \in \{1, \dots, m+n\}$ $\mid \lambda_i(A(X + E(X) \circ A)) \mid - + \lambda_i(\overline{A}(X)) \mid \approx \sum_{i,k} \frac{\partial \mid \lambda_i \mid}{\partial \delta_{i,k}} \Big|_{\Delta = 0} \delta_{j,k}$. (32)

For the derivative $\frac{\partial |\lambda_i|}{\partial \mathbf{\Lambda}} = \left[\frac{\partial |\lambda_i|}{\partial \delta_{i,k}}\right]$, define

$$\left\| \frac{\partial + \lambda_i + }{\partial \mathbf{\Delta}} \right\|_{\text{sum}} \triangleq \sum_{j,k} \left| \frac{\partial + \lambda_i + }{\partial \delta_{j,k}} \right|. \quad (33)$$

Then

$$\|\lambda_{i}(\overline{A}(X + E(X) \circ \Delta))\| - \|\lambda_{i}(\overline{A}(X))\| \le \|\Delta\|_{\max} \|\frac{\partial |\lambda_{i}|}{\partial \Delta}\|_{\Delta = 0}\|_{\sup}.$$
(34)

This leads to the following precision measure for realization X in block-floating-point format

$$\mu_{1}(\boldsymbol{X}) \triangleq \min_{i \in [1, \cdots, m+n]} \frac{1 - |\lambda_{i}(\overline{\boldsymbol{A}}(\boldsymbol{X}))|}{\left\|\frac{\partial |\lambda_{i}|}{\partial \boldsymbol{\Delta}}\right\|_{\boldsymbol{A} = 0}}.$$
 (35)

Obviously, if $\|\Delta\|_{\max} < \mu_1(X)$, then $\|\lambda_i(\overline{A}(X+E(X)\circ\Delta))\| < 1$ which means that the closed – loop remains stable under the FWL error Δ . In other words, for a given X implemented in block-floating-point format with a sufficient dynamic range, the closed-loop can tolerate those FWL perturbations Δ whose norms $\|\Delta\|_{\max}$ are less than $\mu_1(X)$. The larger $\mu_1(X)$ is, the larger the FWL errors the closed-loop system can tolerate. Similar to Eq. (26), from the precision measure $\mu_1(X)$, an estimate of β_u^{\min} is given as

$$\hat{\beta}_{u1}^{\min}(X) \triangleq -\lfloor \log_2 \mu_1(X) \rfloor - 1. \quad (36)$$

The assumption of small Δ is usually valid in practical implementation of digital controllers. Generally speaking, there is no rigorous relationship between $\mu_0(X)$ and $\mu_1(X)$, but $\mu_1(X)$ is connected with a lower bound of $\mu_0(X)$ in some manner: there are "stable perturbation cubes" larger than $\{\Delta: \| \Delta \|_{\max} < \mu_1(X) \}$ while there is no "stable perturbation cube" larger than $\{\Delta: \| \Delta \|_{\max} < \mu_0(X) \}$ (Wu et al., 2001). Hence, in most cases, it is reasonable to take that $\mu_1(X) \leq \mu_0(X)$ and $\hat{\beta}_{u1}^{\min} \geq \hat{\beta}_{u0}^{\min}$. More importantly, unlike the measure $\mu_0(X)$, the value of $\mu_1(X)$ can be computed explicitly. It is easy to see that

$$\left. \frac{\partial + \lambda_i + \partial \mathbf{A}}{\partial \mathbf{A}} \right|_{\mathbf{A} = 0} = \mathbf{E}(\mathbf{X}) \circ \frac{\partial + \lambda_i + \partial \mathbf{A}}{\partial \mathbf{X}}$$
(37)

Let p_i be a right eigenvector of A(X) corresponding to the eigenvalue λ_i . Define

$$\boldsymbol{M_p} \triangleq \begin{bmatrix} \boldsymbol{p}_1 & \boldsymbol{p}_2 & \cdots & \boldsymbol{p}_{m+n} \end{bmatrix} \tag{38}$$

and

$$\mathbf{M}_{\mathbf{y}} \stackrel{\triangle}{=} [\mathbf{y}_1 \quad \mathbf{y}_2 \quad \cdots \quad \mathbf{y}_{m+n}] = \mathbf{M}_{\mathbf{p}}^{-H}$$
(39)

where the superscript H denotes the conjugate transpose operator and y_i is called the reciprocal left eigenvector related to p_i . The following lemma is due to Li (1998).

Lemma 1 Let $\overline{A}(X) = M_0 + M_1 X M_2$ given in Eq. (14) be diagonalizable. Then

$$\frac{\partial \boldsymbol{\lambda}_i}{\partial \boldsymbol{X}} = \boldsymbol{M}_1^{\mathrm{T}} \boldsymbol{y}_i^* \boldsymbol{p}_i^{\mathrm{T}} \boldsymbol{M}_2^{\mathrm{T}}$$
 (40)

where the superscript * denotes the conjugate operation.

The following proposition shows that, given a X, the value of $\mu_1(X)$ can easily be calculated. **Proposition 4** Let A(X) be diagonalizable. Then

$$\mu_{1}(\boldsymbol{X}) = \min_{i \in \{1, \dots, m+n\}} \cdot \frac{1}{|\lambda_{i}| (1 - |\lambda_{i}|)} \frac{1}{\|(\boldsymbol{M}_{1}^{T} \operatorname{Re}[\lambda_{i}^{*} \boldsymbol{y}_{i}^{*} \boldsymbol{p}_{i}^{T}] \boldsymbol{M}_{2}^{T}) \circ \boldsymbol{E}(\boldsymbol{X})\|_{\text{sum}}}$$
(41)

Proof Noting $|\lambda_i| = \sqrt{\lambda_i^* \lambda_i}$ leads to

$$\frac{\partial |\lambda_{i}|}{\partial \mathbf{X}} = \frac{1}{2\sqrt{\lambda_{i}^{*}\lambda_{i}}} \left(\frac{\partial \lambda_{i}^{*}}{\partial \mathbf{X}} \lambda_{i} + \lambda_{i}^{*} \frac{\partial \lambda_{i}}{\partial \mathbf{X}} \right) =
\frac{1}{2|\lambda_{i}|} \left(\left(\frac{\partial \lambda_{i}}{\partial \mathbf{X}} \right)^{*} \lambda_{i} + \lambda_{i}^{*} \frac{\partial \lambda_{i}}{\partial \mathbf{X}} \right) =
\frac{1}{|\lambda_{i}|} \operatorname{Re} \left[\lambda_{i}^{*} \frac{\partial \lambda_{i}}{\partial \mathbf{X}} \right].$$
(42)

Combining Eqs. (35), (37), (42) and Lemma 1 results in this proposition.

Replacing $\mu_0(X)$ with $\mu_1(X)$ in Eq. (28) leads to a computationally tractable FWL closed-loop stability measure

$$\rho_1(\mathbf{X}) \triangleq \mu_1(\mathbf{X})/\gamma(\mathbf{X}). \tag{43}$$

From the measure $\rho_1(\textbf{\textit{X}})$, an estimate of β^{\min} is given as

$$\hat{\beta}_1^{\min}(X) \triangleq -\lfloor \log_2 \rho_1(X) \rfloor + 1 . \quad (44)$$

OPTIMIZATION PROCEDURE

As different realizations X have different values of the FWL closed-loop stability measure $\rho_1(X)$, it is of practical importance to find an "optimal" realization $X_{\rm opt}$ that maximizes $\rho_1(X)$. The controller implemented with this optimal realization $X_{\rm opt}$ needs a minimum bit length and has a maximum tolerance to the FWL error in block-floating-point format. This optimal controller realization problem is formally defined as

$$v \stackrel{\triangle}{=} \max_{\mathbf{x} \in \mathcal{S}} \rho_1(\mathbf{X}). \tag{45}$$

Assume that a controller has been designed using some standard controller design method. This controller, denoted as

$$\boldsymbol{X}_0 \stackrel{\triangle}{=} \begin{bmatrix} \boldsymbol{M}_0 & \boldsymbol{J}_0 \\ \boldsymbol{G}_0 & \boldsymbol{F}_0 \end{bmatrix}, \tag{46}$$

is used as the initial controller realization in the above optimal controller realization problem. Let p_{0i} be a right eigenvector of $\overline{A}(X_0)$ corresponding to the eigenvalue λ_i , and y_{0i} be the reciprocal left eigenvector related to p_{0i} . The definition of S_C in Eq.(12) means that

$$X \stackrel{\triangle}{=} X(T) = \begin{bmatrix} I & \mathbf{0} \\ \mathbf{0} & T^{-1} \end{bmatrix} X_0 \begin{bmatrix} I & \mathbf{0} \\ \mathbf{0} & T \end{bmatrix}$$
(47)

where $det(T) \neq 0$. It can then be shown that

$$\overline{A}(X) = \begin{bmatrix} I & \mathbf{0} \\ \mathbf{0} & T^{-1} \end{bmatrix} \overline{A}(X_0) \begin{bmatrix} I & \mathbf{0} \\ \mathbf{0} & T \end{bmatrix}$$
(48)

which implies that

$$\boldsymbol{p}_{i} = \begin{bmatrix} \boldsymbol{I} & \boldsymbol{0} \\ \boldsymbol{0} & \boldsymbol{T}^{-1} \end{bmatrix} \boldsymbol{p}_{0i}, \ \boldsymbol{y}_{i} = \begin{bmatrix} \boldsymbol{I} & \boldsymbol{0} \\ \boldsymbol{0} & \boldsymbol{T}^{\mathrm{T}} \end{bmatrix} \boldsymbol{y}_{0i}.$$
 (49)

Hence

$$\mathbf{M}_{1}^{\mathrm{T}} \operatorname{Re} \left[\lambda_{i}^{*} \mathbf{y}_{i}^{*} \mathbf{p}_{i}^{\mathrm{T}} \right] \mathbf{M}_{2}^{\mathrm{T}} = \\
\begin{bmatrix} \mathbf{I} & \mathbf{0} \\ \mathbf{0} & \mathbf{T}^{\mathrm{T}} \end{bmatrix} \mathbf{M}_{1}^{\mathrm{T}} \operatorname{Re} \left[\lambda_{i}^{*} \mathbf{y}_{0i}^{*} \mathbf{p}_{0i}^{\mathrm{T}} \right] \mathbf{M}_{2}^{\mathrm{T}} \left[\mathbf{I} & \mathbf{0} \\ \mathbf{0} & \mathbf{T}^{\mathrm{T}} \right] \mathbf{\Phi}_{i} \left[\mathbf{I} & \mathbf{0} \\ \mathbf{0} & \mathbf{T}^{\mathrm{T}} \right] \right] \leq (50)$$

with $\boldsymbol{\Phi}_{i} = \boldsymbol{M}_{1}^{T} \operatorname{Re} \left[\lambda_{i}^{*} \boldsymbol{y}_{0i}^{*} \boldsymbol{p}_{0i}^{T} \right] \boldsymbol{M}_{2}^{T}$. Define the following cost function:

$$f(T) \triangleq \min_{i \in \{1, \dots, m+n\}} \bullet$$

$$\left(\frac{\left\|\left(\begin{bmatrix}\boldsymbol{I} & \boldsymbol{0} \\ \boldsymbol{0} & \boldsymbol{T}^{\mathrm{T}}\end{bmatrix}\boldsymbol{\Phi}_{i}\begin{bmatrix}\boldsymbol{I} & \boldsymbol{0} \\ \boldsymbol{0} & \boldsymbol{T}^{-\mathrm{T}}\end{bmatrix}\right) \circ \boldsymbol{E}(\boldsymbol{X}(\boldsymbol{T}))\right\|_{\mathrm{sum}}}{\left\|\lambda_{i}\right\|\left(1-\left\|\lambda_{i}\right\|\right)}\right)$$

$$\log_2 \frac{4 \parallel \boldsymbol{q}(\boldsymbol{X}(\boldsymbol{T})) \parallel_{\text{max}}}{\pi(\boldsymbol{q}(\boldsymbol{X}(\boldsymbol{T})))}\right)^{-1}.$$
 (51)

Then the optimal controller realization problem Eq.(45) can be posed as the following optimization problem:

$$v = \max_{\substack{T \in R^{n \times m} \\ d \neq T \neq 0}} f(T). \tag{52}$$

As the optimization problem Eq. (52) is highly nonlinear, global optimization algorithms, such as the genetic algorithm and adaptive simulated annealing, can be adopted to provide a (sub)optimal similarity transformation $T_{\text{opt}}(\alpha)$. Global optimization methods are however computationally demanding. Local optimization algorithms, such as Rosenbrock and Simplex algorithms, are computationally simpler but run more risks of only attaining a local solution. Our experience with the optimization problem Eq. (29) suggests that local optimization methods are usually efficient in controllers of low order while global optimization methods have to be adopted in controllers of high order. It also helps to choose a "good" initial controller realization, such as Li's closed-loop sub-optimal realization (Li, 1998), as the initial guess for the optimization routine. With the solution $m{T}_{ ext{opt}}$ of optimization problem Eq. (52), the optimal realization X_{opt} can readily be computed.

DESIGN EXAMPLE

An example is used to illustrate the design procedure based on the FWL closed-loop stability measure. In this example, the discrete-time plant is given by

$$A = \begin{bmatrix} 3.7156e + 0 & -5.4143e + 0 & 3.6525e + 0 & -9.6420e - 1 \\ 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 1 & 0 \end{bmatrix}$$

$$\boldsymbol{B} = \begin{bmatrix} 1 & 0 & 0 & 0 \end{bmatrix}^{\mathrm{T}},$$

$$C = [1.1160e - 6 \quad 4.3000e - 8 \quad 1.0880e - 6 \quad 1.4000e - 8].$$

The initial realization of the digital controller is given by

$$F_0 = \begin{bmatrix} 2.6963e + 2 & -4.2709e + 1 & 2.2873e + 1 & 2.6184e + 2 \\ 2.5561e + 2 & -4.0497e + 1 & 2.1052e + 1 & 2.4806e + 2 \\ 5.6096e + 1 & -8.5715e + 0 & 5.2162e + 0 & 5.4920e + 1 \\ -2.3907e + 2 & 3.7998e + 1 & -2.0338e + 1 & -2.3203e + 2 \end{bmatrix}$$

$$G_0 = \begin{bmatrix} G_0 = 1 & 0.0286 & 0.0288 & 0.02$$

$$\begin{bmatrix} -4.6765e + 1 & -4.5625e + 1 & -9.5195e + 0 & 4.1609e + 1 \end{bmatrix}^{T},$$

 $J_0 =$ [-2.5548e + 2 -2.7185e + 2 -2.7188e + 2 2.7188e + 2], $M_0 =$ [0].

Based on the proposed FWL closed-loop stability measure, the optimization problem Eq. (52) is formed. Using the MATLAB routine *fminsearch*. m for the controller of 4 order, this optimization problem is solved to obtain the optimal similarity transformation

$$\begin{split} & \boldsymbol{T}_{\text{opt}} = \\ & \begin{bmatrix} -1.0345\mathrm{e} - 001 & 1.2904\mathrm{e} - 001 & 3.8329\mathrm{e} - 003 & 1.0911\mathrm{e} - 002 \\ -1.1078\mathrm{e} - 001 & 1.1742\mathrm{e} - 001 & 2.9461\mathrm{e} - 003 & 8.1639\mathrm{e} - 003 \\ -2.3775\mathrm{e} - 002 & 2.3815\mathrm{e} - 002 & 4.9498\mathrm{e} - 004 & 1.8293\mathrm{e} - 003 \\ 9.2138\mathrm{e} - 002 & -1.1474\mathrm{e} - 001 & -3.4007\mathrm{e} - 003 & -9.6780\mathrm{e} - 003 \end{bmatrix}. \end{split}$$

It is obvious that the true minimum block exponent bit length $\beta_h^{\min}(X)$ for a realization X can directly be obtained by examining the elements of X. The true minimum block mantissa bit length $\beta_u^{\min}(X)$ however can only be obtained through simulation. That is, starting from a very large β_u , reduce β_u by one bit and check the closed-loop stability. The process is repeated until there appears closed-loop instability at $\beta_u = \beta_{uu}$. Then $\beta_u^{\min} = \beta_{uu} + 1$. Table 1 summarizes

Table 1 Various measures and bit lengths for X and X_{cort}

A opt		
	$oldsymbol{X}_0$	$oldsymbol{X}_{ ext{opt}}$
$\rho_1(X)$	1.5154e – 11	4.7787e - 6
$\hat{eta}_1^{\min}(extbf{ extit{X}})$	37	19
$\mu_1(\boldsymbol{X})$	6.8793e - 11	3.6388e - 5
$\hat{eta}_{u1}^{\min}(oldsymbol{X})$	33	14
$\gamma(X)$	4.5395e + 0	7.6146e + 0
$\hat{eta}_h^{\min}(oldsymbol{X})$	3	3
$eta^{\min}(extbf{ extit{X}})$	33	16
$eta_u^{\min}(oldsymbol{X})$	30	12
$\beta_h^{\min}(X)$	2	3

the various measures, the corresponding estimated minimum bit lengths and the true minimum bit lengths for the controller realizations X_0 and $X_{\rm opt}$. It can be seen that $X_{\rm opt}$ improves the measure ρ_1 by a factor of 300000 over X_0 and that the block-floating-point implemented X_0 needs at least 33 bits while the implementation of $X_{\rm opt}$ needs at least 16 bits. More than half of the bit length is saved.

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