



## High-efficiency technique based on dielectrophoresis for assembling metal, semiconductor, and polymer nanorods\*

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**Abstract:** This paper presents a high-efficiency technique based on dielectrophoresis (DEP) for assembling metal, semiconductor, and polymer nanorods, which are synthesized by electrochemical deposition (ECD). The assembly patterns of these nanorods (width: 20 nm; length: 7  $\mu\text{m}$ ) were designed using a finite element method (FEM) simulation tool. Further, these nanorods were used in our experiment after their assembly patterns were fabricated. The assembly yield was found to be approximately 70% at an AC voltage of 30 V<sub>p-p</sub> and at frequencies of 20 and 30 kHz, and the DC voltage prevented the random alignment of the nanorods at the edge of the assembly pattern. Moreover, the above-mentioned nanorods, which had different permittivities, were found to have similar assembly yields. The proposed method can be improved and applied to nanostructure device fabrication.

**Key words:** Nanorod, Assembly, Dielectrophoresis (DEP), Finite element method (FEM), Electrochemical deposition (ECD) method

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### 1 Introduction

In recent years, nanoscience and nanotechnology have been extensively researched in fields such as materials science, chemistry, mechanics, biology, and medicine. Nanostructures exhibiting novel properties due to a quantum confinement effect (Brus, 1983), such as nanoparticles (Wang *et al.*, 2007), nanotubes (Spitalsky *et al.*, 2010), nanowires (Xia *et al.*, 2010), nanorods (He *et al.*, 2010), and nanopores (Madampane *et al.*, 2010), have been studied and used in

various fields.

An assembly technique used to arrange nanorods at user-defined positions is necessary for the fabrication of nanodevices consisting of nanorods. Nanorods synthesized by electrochemical deposition (ECD) (Wirtz and Martin, 2003), in particular, are generally dispersed in a liquid medium after they are synthesized. Therefore, it is essential to develop a technique to assemble nanorods. Dielectrophoresis (DEP) has been widely used for carbon nanotube (CNT) assembly (Inoue *et al.*, 2010) and selective DNA separation (Yokokawa *et al.*, 2010). However, it is difficult to assemble high-density nanorods such as gold (Au) nanorods, owing to the fact that these nanorods cohere with each other because of the surface tension generated when they are dispersed in a solvent.

In this study, we synthesized three types of nanorods—metal, semiconductor, and polymer

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nanorods. Then, we designed the assembly patterns of these nanorods after carrying out electric field simulations. We also evaluated the assembly performance of these nanorods. After fabricating the assembly patterns, we performed nanorod assembly experiments and compared the results.

## 2 Principle of dielectrophoresis

According to the principle of DEP, an additional force acts on a particle when a heterogeneous electric field is applied to the particle. The dielectrophoretic force and the moving direction are determined on the basis of the strength of the electric field, frequency of the source, size and shape of the particle, and permittivities of the particle and medium (Jones and Kallio, 1979). The dielectrophoretic force acting on a particle in an electric field is given as (Pethig, 1996)

$$F_{\text{DEP}} = \Gamma \cdot \varepsilon_m \operatorname{Re}[K(\omega)] \nabla |E|^2, \quad (1)$$

where  $\Gamma$  is the geometry coefficient,  $K(\omega)$  is the Clausius-Mossotti factor, and  $\nabla |E|^2$  is the gradient of the energy density of the electric field. In the case of a cylindrical nanorod, Eq. (1) is rewritten as

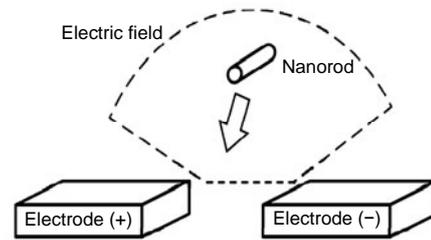
$$F_{\text{DEP}} = \frac{3\pi}{4} a^2 l \cdot \varepsilon_m \operatorname{Re} \left[ \frac{\varepsilon_p^* - \varepsilon_m^*}{\varepsilon_p^* + 2\varepsilon_m^*} \right] \nabla |E|^2, \quad (2)$$

where  $a$  is the radius of nanorods' section,  $l$  is the length of nanorods,  $\varepsilon_p$  and  $\varepsilon_m$  are the permittivities of the nanorod and medium, respectively. According to the principle of DEP, if the real part of the Clausius-Mossotti factor  $\operatorname{Re}[K(\omega)] > 0$ , the nanorod can move to a region that has positive dielectrophoresis (pDEP), and if  $\operatorname{Re}[K(\omega)] < 0$ , the nanorod can move to a region with negative dielectrophoresis (nDEP). A schematic of the nanorod assembly fabricated by DEP is shown in Fig. 1. This figure shows that a nonuniform electric field is formed between two electrodes. It also shows that a DEP force acts on the nanorod.

## 3 Experiments and results

### 3.1 Nanorod synthesis

The nanorods used in the experiment were synthesized using an anodic aluminum oxide (AAO)



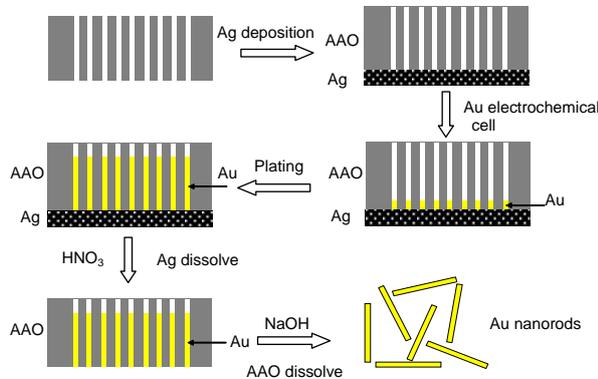
**Fig. 1** Schematic diagram of nanorod assembly by the dielectrophoresis

template (Anodisc<sup>13TM</sup>, Whatman Inc., USA) by ECD. Anodisc<sup>13TM</sup>, which has a uniform pore size of approximately 200 nm, is usually used as a filter. The radius of the nanorods cannot be controlled if an AAO template is used. However, their length and type, such as a metal, a semiconductor, or a macromolecule, can be controlled, and we can also synthesize nanorods comprising several materials with varying compositions (Ag-Au-Ag, Ag-polypyrrole (PPY)-Au, etc.).

First, an Ag layer (70-nm or 100-nm thick) was deposited on one side of the AAO template, which was used as the working electrode. A Pt wire and an Ag/AgCl electrode were used as the counter electrode and the reference electrode, respectively. Then Ag nanorods were synthesized using an Ag-plating solution (Technic Silver 1025, Technic Inc., USA) at  $-800$  mV for 1 C (10 min). Au nanorods were synthesized using an Au-plating solution (Orotemp 24 Gold Salts, Technic Inc., USA) at  $-850$  mV for 7 C (7 h). After that, the template was immersed in HNO<sub>3</sub> solution (90%, v/v) for 1 h to melt the Ag layer. Then the template was dissolved by immersion in 3 mol/L NaOH for approximately 5 h. After the synthesized nanorods were centrifuged and rinsed several times, they were suspended in an aliquot of EtOH.

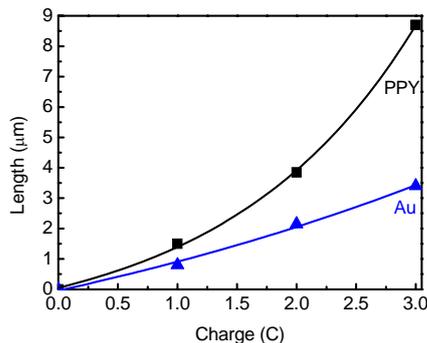
Polymer nanorods were synthesized by electropolymerization using a plating solution (0.1 mol/L pyrrole, and 0.1 mol/L tetraethylammonium tetrafluoroborate in acetonitrile) at  $\pm 900$  mV for 2.5 C (30 min). CdSe nanorods were fabricated by a cyclic voltammetry (CV) method and then electrodeposited using a plating solution (0.25 mol/L sulfuric acid, 0.03 mol/L cadmium sulfate, and 0.7 mmol/L selenium dioxide in deionized water) by sweeping the voltage between  $-350$  and  $-800$  mV (20 h). These nanorods were deposited layer by layer in the pores of the AAO template for 56000 cycles. Fig. 2 shows the synthesis process of Au nanorods. The synthesis

processes of the PPY and CdSe nanorods were similar to that of Au nanorods.



**Fig. 2 Schematic process of Au nanorods**

The growth rate of each type of nanorod based on the induced charge is shown in Fig. 3. The growth rate of the CdSe nanorods is not very high, because these nanorods are synthesized by the CV method.



**Fig. 3 Growth rate of Au and polypyrrole (PPY) nanorods**

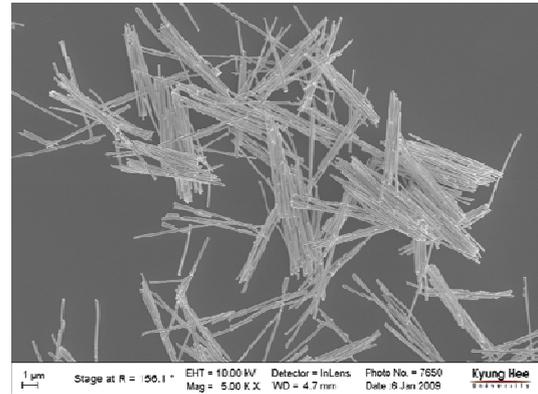
CdSe nanorods are not described because they are synthesized by cycle voltammetry

The nanorods used in the assembly experiment were 7- $\mu\text{m}$  long and 200-nm wide. The scanning electron microscope (SEM) image of the synthesized Au nanorods is shown in Fig. 4.

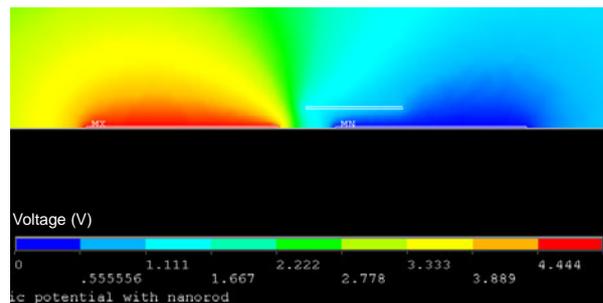
### 3.2 Assembly simulation and fabrication

On the basis of the DEP principle, ANSYS<sup>TM</sup>, a well-known finite element method (FEM) simulation tool, was used to design and optimize the nanorod assembly prior to carrying out the nanorod assembly experiment. First, on the basis of the assumption that the assembly pattern of the nanorod was made of Au, assembly simulation was carried out for optimizing

the gap between the assembly patterns (where the nanorods were assembled). Chloroform was used as the media solution to preserve the nanorods. The side view of simulation result of the DEP acting on a nanorod is shown in Fig. 5.



**Fig. 4 SEM image of the synthesized Au nanorods**

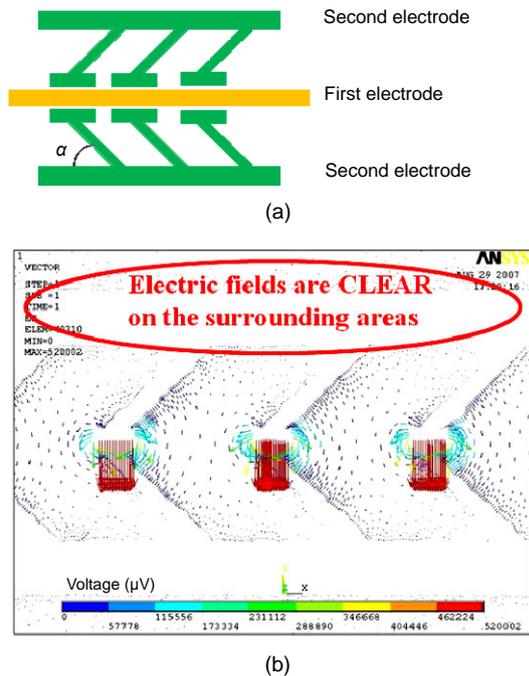


**Fig. 5 Side view of simulation result of DEP acting on a nanorod**

Voltages of +5 V and 0 V were applied to the left and right electrodes, respectively (Fig. 5). The simulation result shows that the electric field bends around the nanorods. The nanorod assembly pattern was designed after the nanorod assembly was optimized. The assembly pattern of the synthesized nanorods is shown in Fig. 6a. We supplied voltages of +5 V and 0 V to the first and second electrodes, respectively. The simulation result is shown in Fig. 6b.

As shown in Fig. 6, the result shows that the electric field is concentrated at certain sites, and the field strength is minimum at the edge of the electrode where the angle  $\alpha$  is equal to 45°.

Then, from the simulation results, the assembly patterns of the nanorods were fabricated using a conventional lift-off process. First, an oxide layer (1000-nm thick) was grown on a p-type silicon wafer



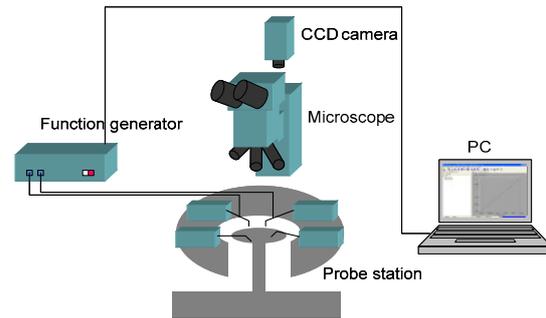
**Fig. 6** Schematic (a) and electric field (b) simulations of the proposed nanorod assembly electrode

by wet thermal oxidation to prevent current leakage. After patterning the photoresist using AZ 5214, a Ni layer (15-nm thick) and an Au layer (135-nm thick) were deposited on the wafer using e-beam evaporation. The photoresist was then removed using acetone. Finally, a  $\text{Si}_3\text{N}_4$  layer (150-nm thick) was deposited on the electrode to isolate the nanorods from the electrodes. The gap between AC and DC electrodes of the fabricated electrode assembly is approximately 5  $\mu\text{m}$ .

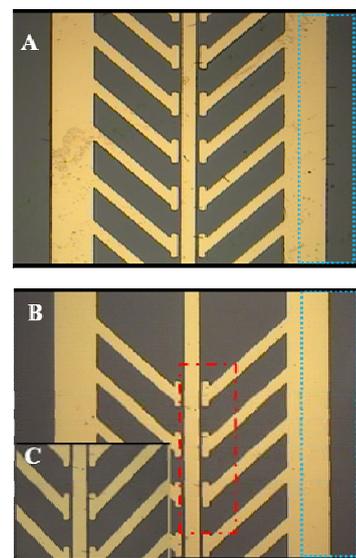
### 3.3 Nanorod assembly experiments

The nanorod assembly system used in this study is shown in Fig. 7. This system consists of a probe station, a function generator, a microscope, a lamp, a charge coupled device (CCD) camera, and a PC. After fixing the fabricated nanorod assembly at the center of the probe station, power was supplied to the nanorods through the probe station from the function generator. The experimental assembly results can be observed on the PC.

The nanorod assembly experiment was performed to verify the simulation results. The microscope images of the nanorods array which under the condition of 20  $V_{\text{p-p}}$ , 20 kHz (AC) and 10 V (DC) are shown in Figs. 8a and 8b, respectively.



**Fig. 7** Schematic of nanorod assembly system



**Fig. 8** Optical microscope images obtained after nanorod assembly

(a) Without applying DC voltage (AC of 20  $V_{\text{p-p}}$ , 20 kHz); (b) Applying a DC voltage of 10 V; (c) Scale-up image of the left box of (b). The right box of (b) denotes the random placement of the nanorods at the edge of the electrode

Figs. 8a and 8b show that the nanorods were accurately aligned with the application of the DC voltage as compared to that without the DC voltage. Moreover, the DC electrode could prevent the random alignment of the nanorods at the edge of the electrodes. This result is in good agreement with the simulation result. The SEM image of the assembled Au nanorod is shown in Fig. 9. An Au nanorod is assembled in the gap between the two electrodes (up and down blocks).

To evaluate the assembly performance of the proposed electrode, three types of nanorods—metal (gold), polymer (PPY), and semiconductor (CdSe) nanorods—having different permittivities were used in the nanorod assembly experiment, because the

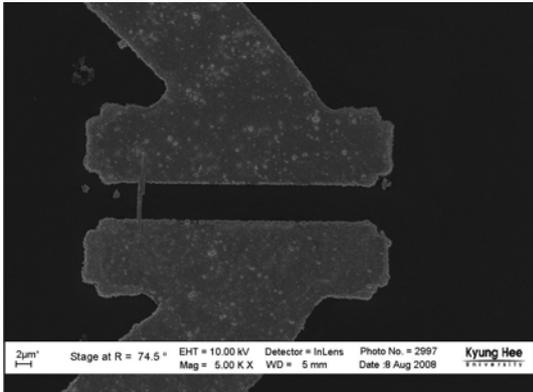


Fig. 9 SEM image of nanorod assembly

permittivity of the nanorods as well as that of the medium affects the dielectrophoretic force. The assembly experiment was performed by applying an AC voltage of  $30 V_{p-p}$  at frequencies ranging from 10 kHz to 1 GHz at the AC electrode and a DC voltage of 10 V at the DC electrode (Fig. 10).

Fig. 10a shows a plot of the assembly yield against frequency. The assembly yield was the highest, i.e., 70.8%, at frequencies of 20 and 30 kHz, and it gradually decreased above 30 kHz. The assembly yield was less than 20% below 10 kHz. In (Inoue *et al.*, 2010), the assembly performance of CNTs or nanowires synthesized by DEP has been reported to be high when the frequency applied was around several gigahertz. However, in our experiment, the assembly yield was the highest at around 20 and 30 kHz.

The assembly performances of nanorods made of various materials are shown in Fig. 10a. The figure shows that the assembly yields of metal, polymer, and semiconductor nanorods are similar and tend to decrease above 30 kHz. It is assumed that since the nanorods used in our experiment have a high density, the low frequency at the assembly electrode could have resulted in slow movement of the nanorod and high assembly yield. The similar assembly yield of the three types of nanorods suggests that the permittivity of the medium,  $\epsilon_m$ , and not the permittivity of the nanorods as indicated by Eq. (2), is a dominant factor, because the nanorods used in our experiment have a high density and a large size. Fig. 10b shows a plot of the AC amplitude against the assembly yield. The frequency was fixed at 30 kHz, and a small DC offset was induced at the DC electrode, which reduced the effect of the DC electrode. The assembly

yield increased with the amplitude, as predicted by the DEP principle.

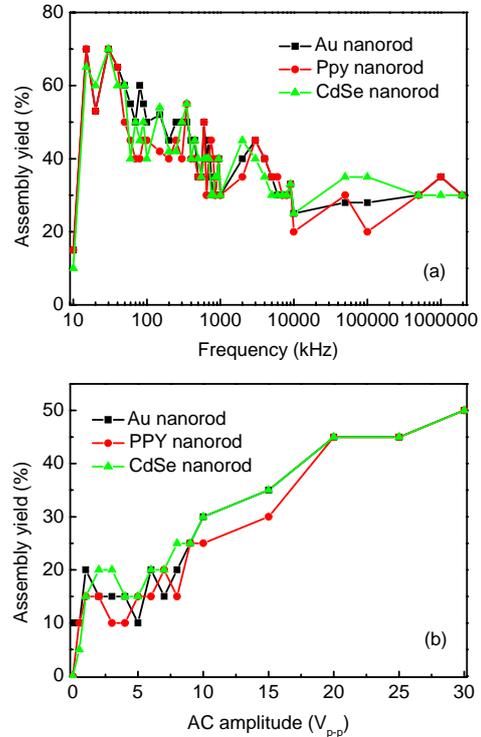


Fig. 10 Plot of assembly yield against (a) induced frequency on the AC electrode (at an amplitude of  $30 V_{p-p}$  and a DC voltage of 10 V) and (b) AC amplitude on the DC electrode (at a frequency of 30 kHz)

## 4 Conclusions

This paper presented a high-efficiency nanorod assembly technique based on DEP. The assembly electrode was designed after carrying out electric field simulations. The nanorod assembly yield was measured under different frequencies and amplitude conditions of the applied voltage. Further, a nanorod assembly experiment was performed using not only the polymer and semiconductor nanorods but also metal nanorods, which are difficult to assemble.

The assembly yield obtained using our assembly pattern was 70.8% at frequencies of 20 and 30 kHz and at an AC voltage of  $30 V_{p-p}$ . Furthermore, the assembly yields of the three types of nanorods were similar. The proposed assembly pattern can be used for the fabrication of nanorod arrays and the development of measurement kits for nanorods and nanowires made of novel materials.

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